



# Replacement Sheet

1/20

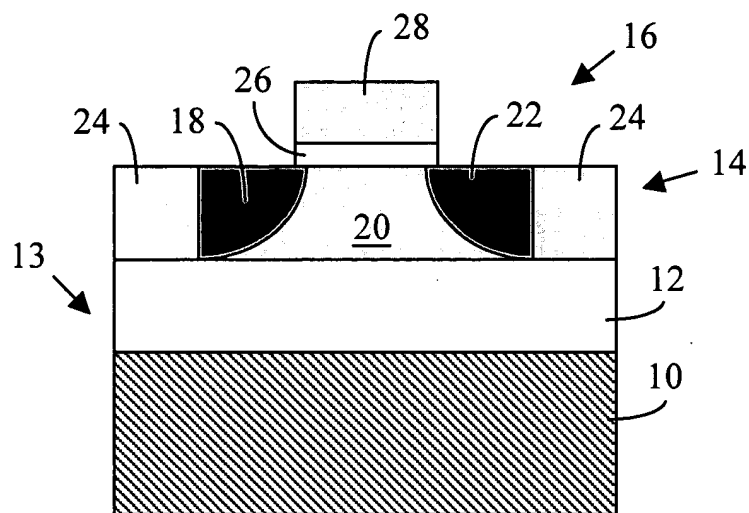


Fig. 1

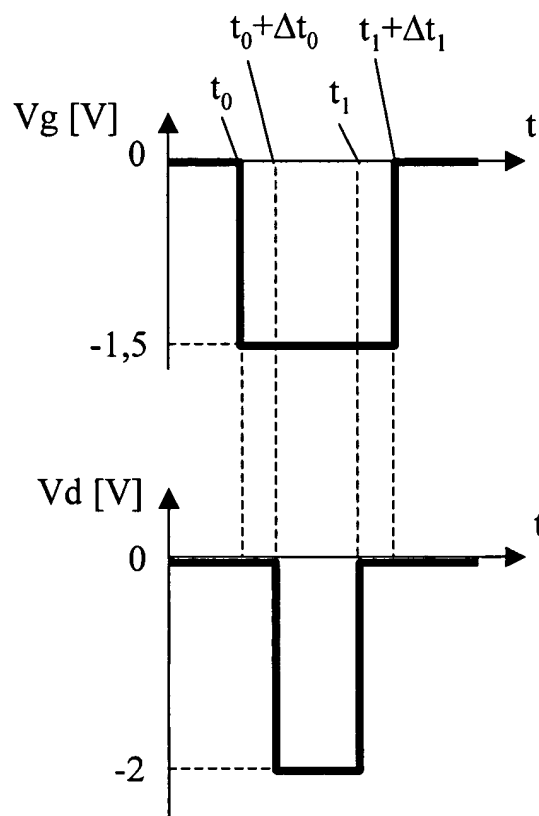


Fig. 2

Fig. 5b

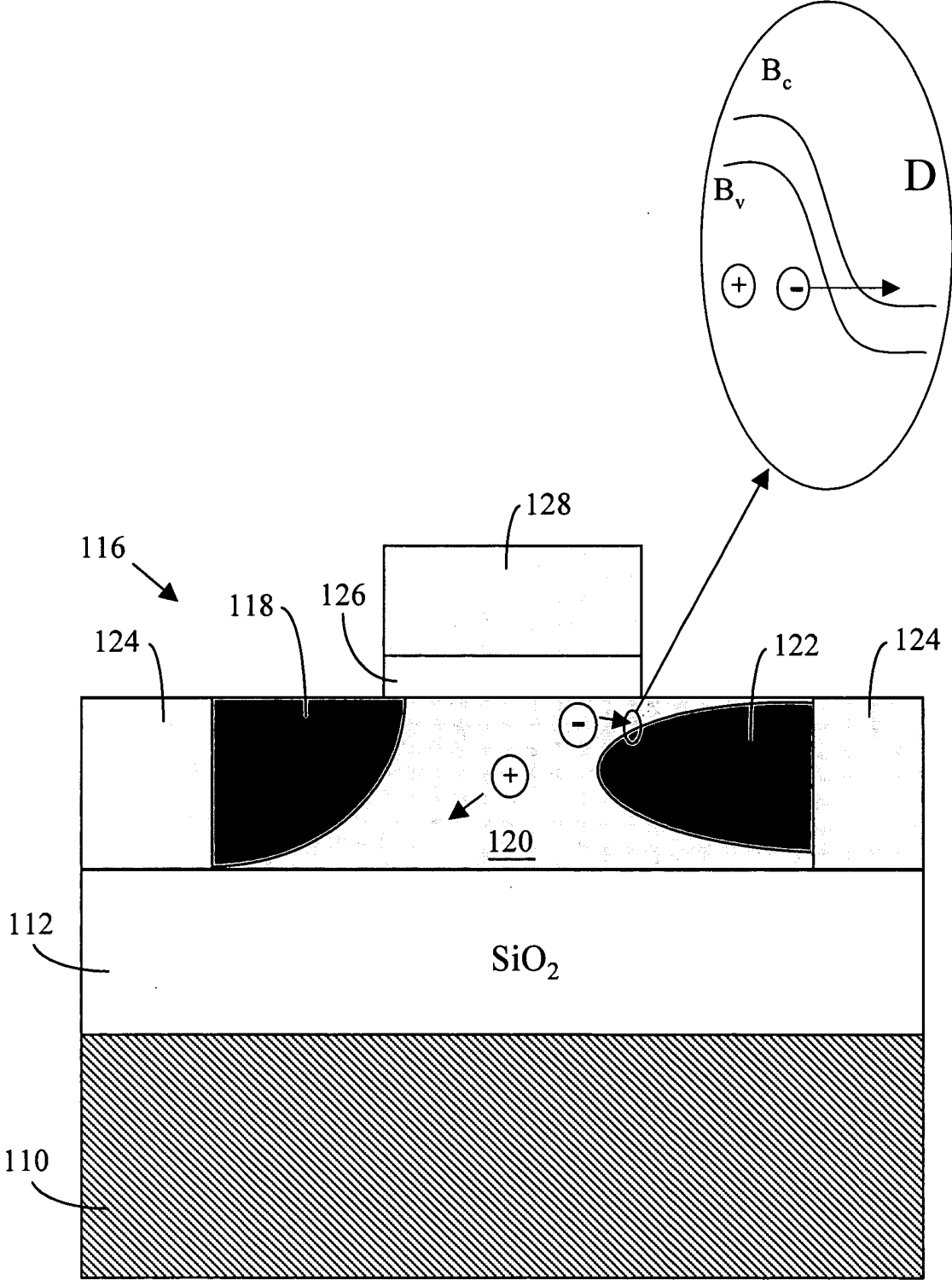


Fig. 5a

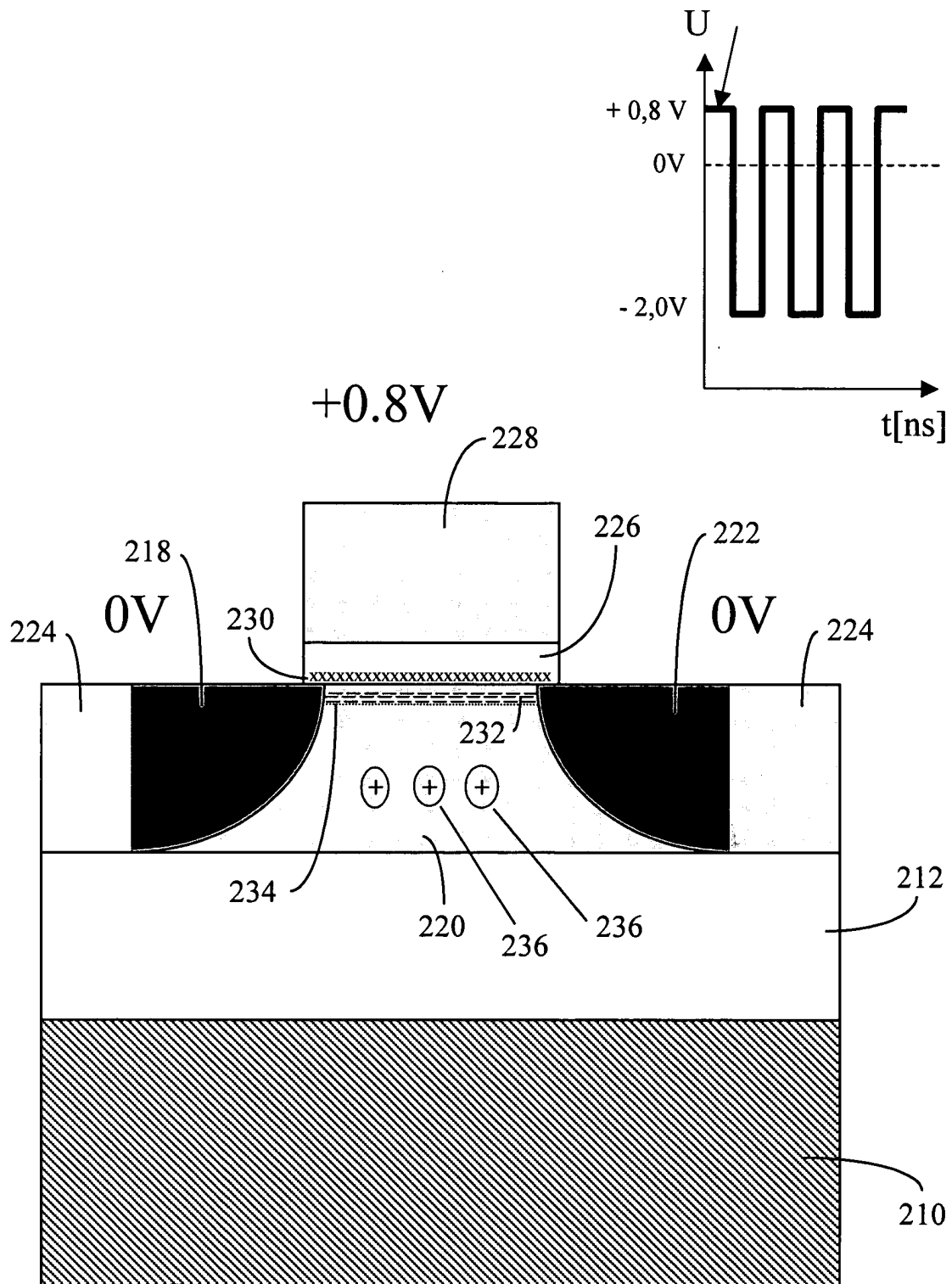


Fig. 6a

5/20

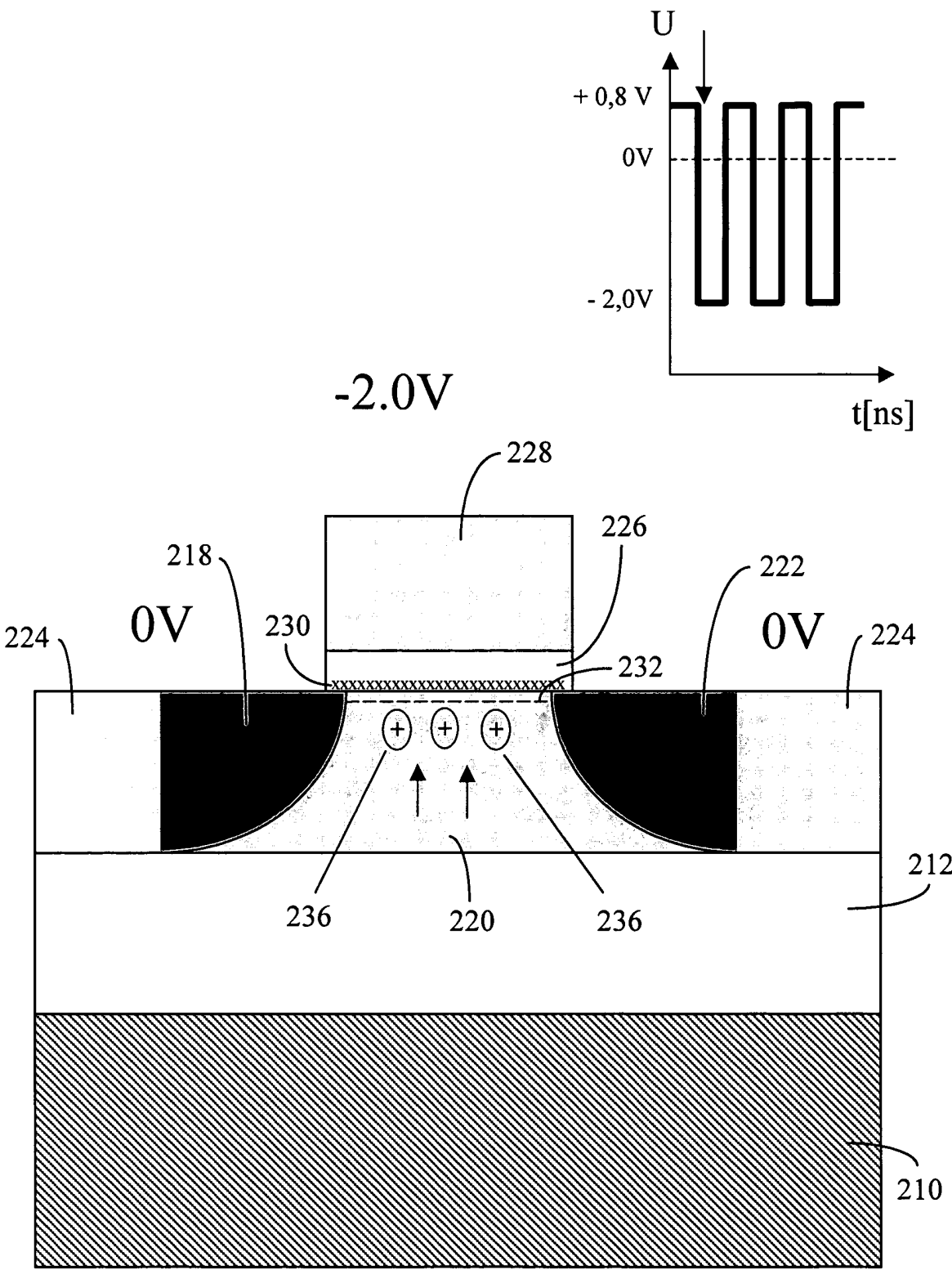


Fig. 6b

6/20

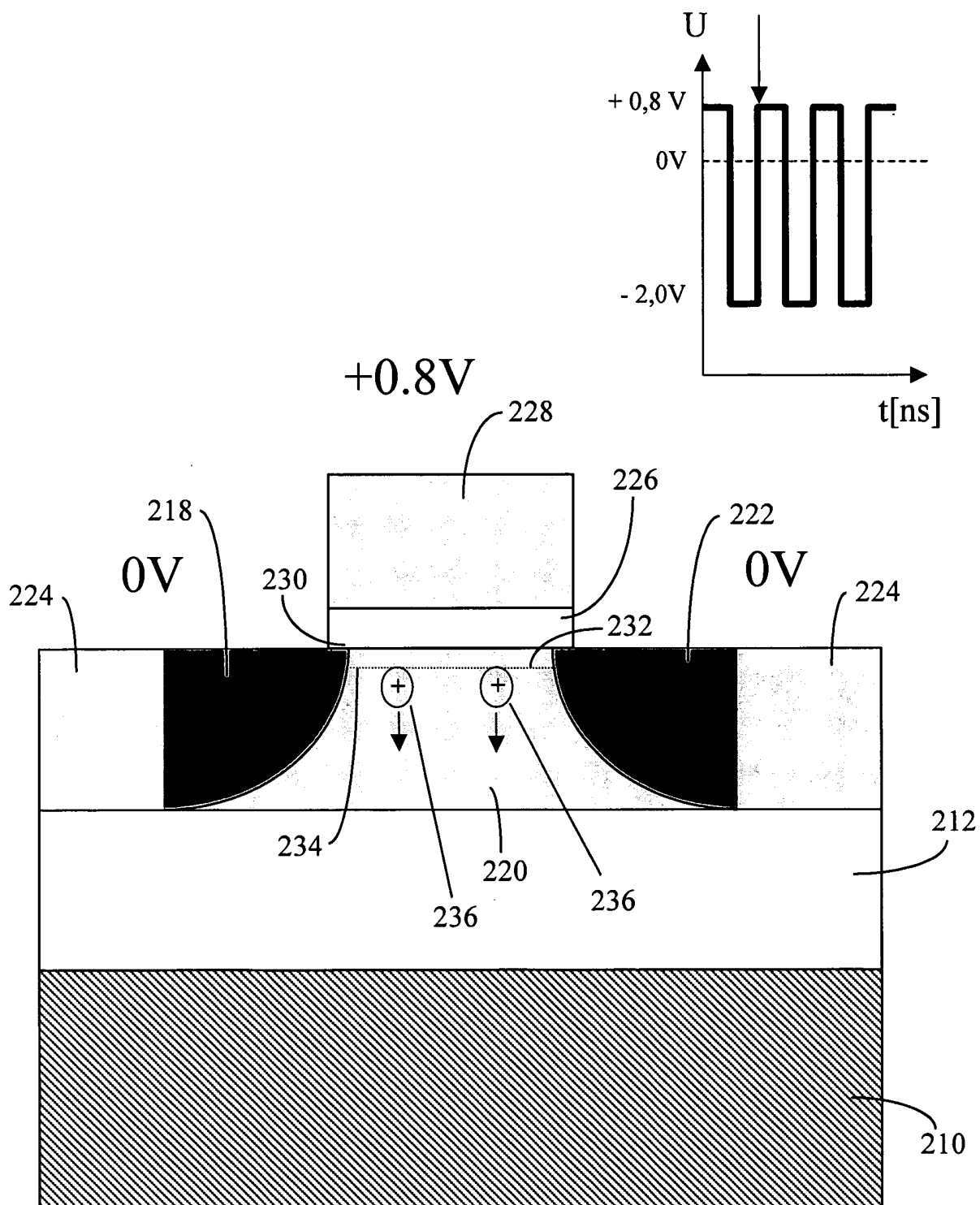


Fig. 6c

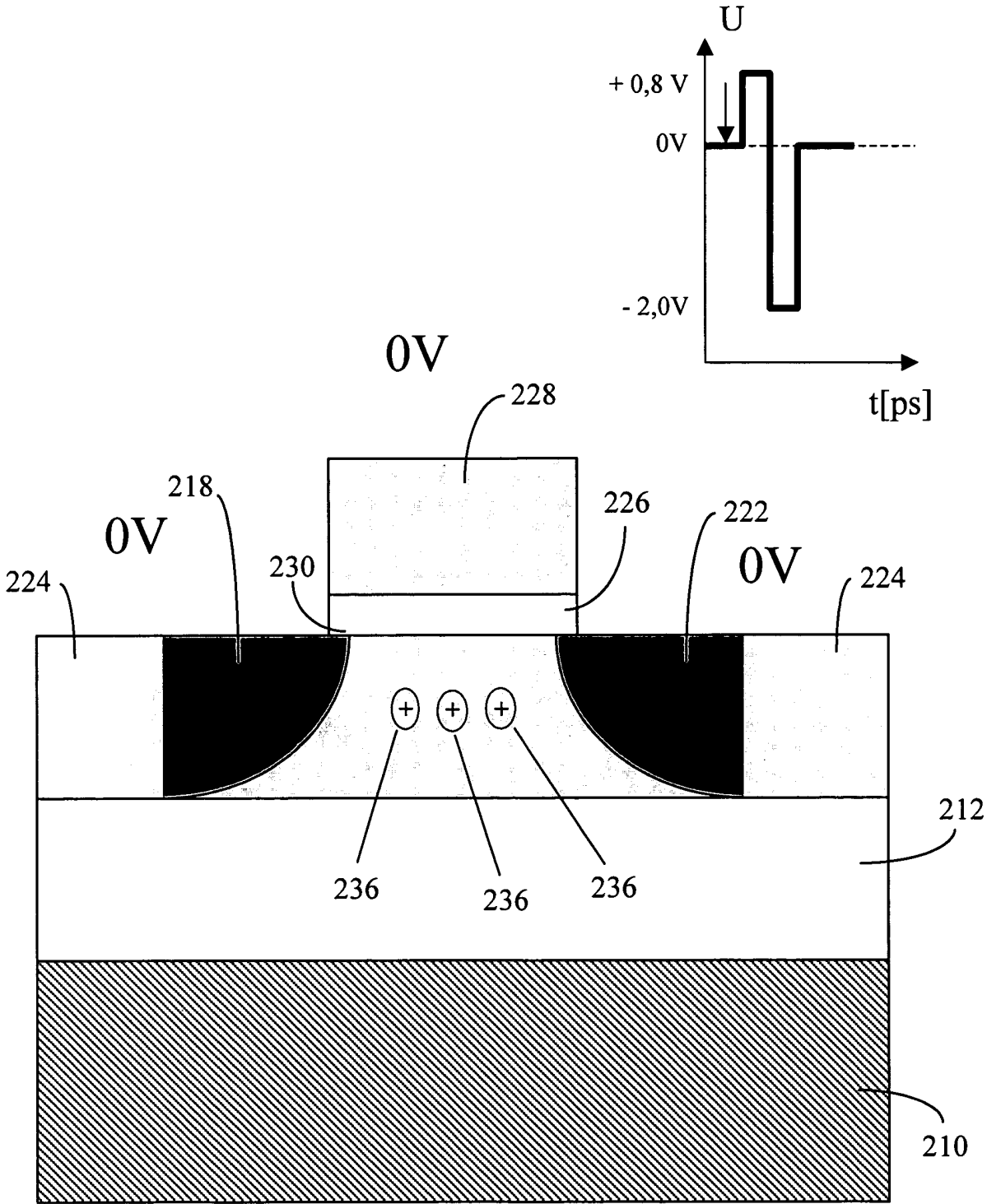


Fig. 7a

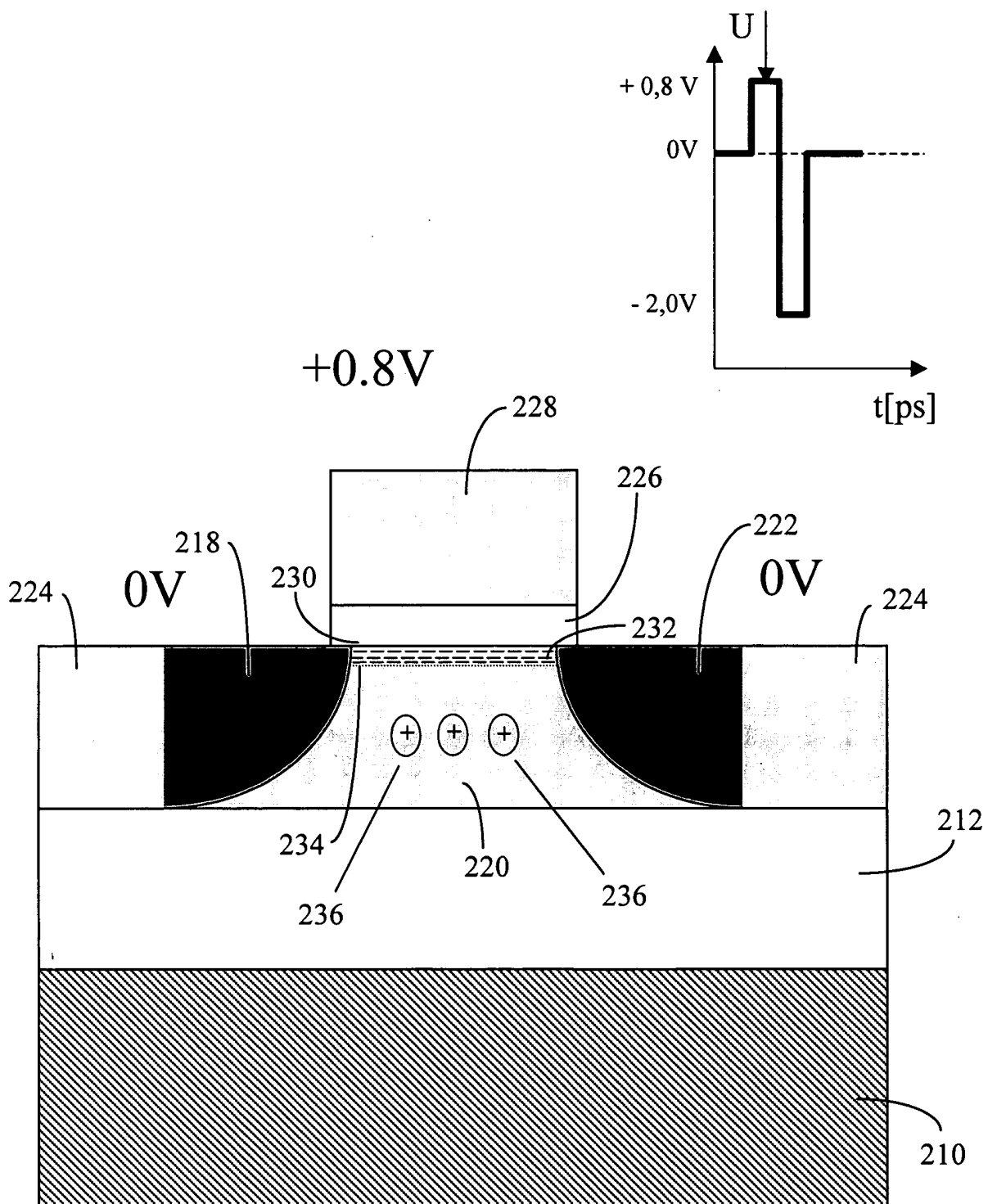


Fig. 7b

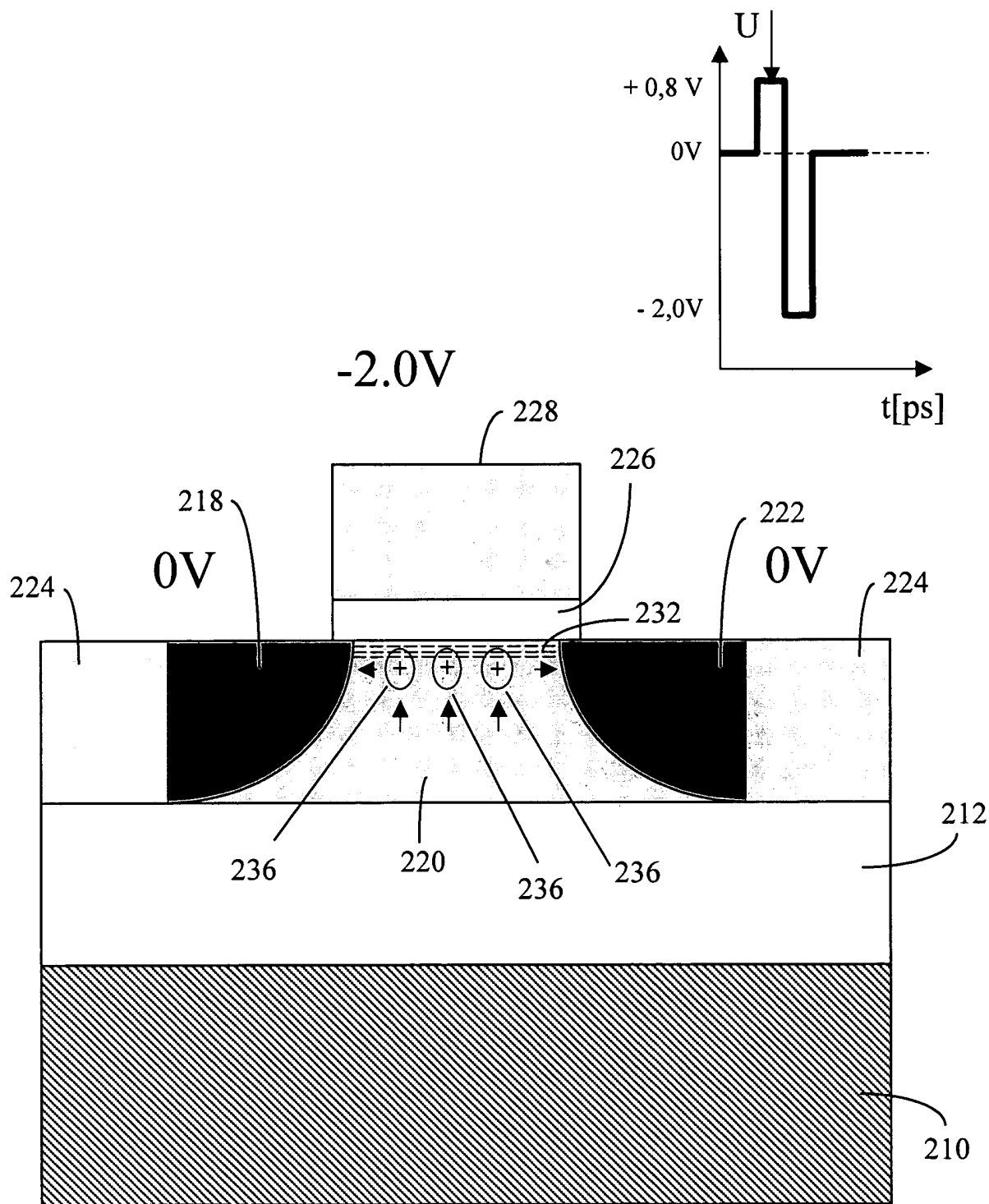


Fig. 7c



10/20

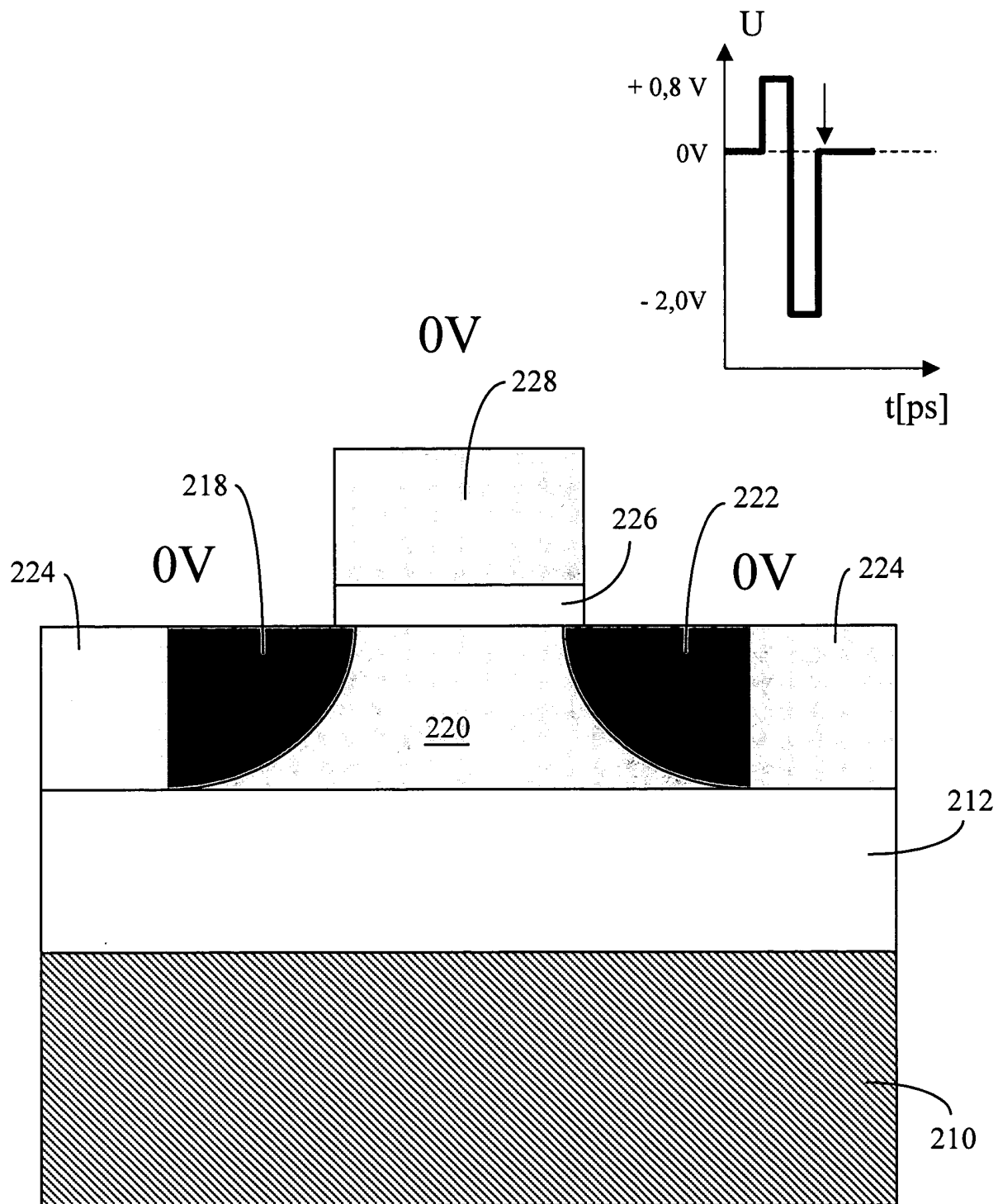


Fig. 7d

**PD-SOI**  
**NMOS**

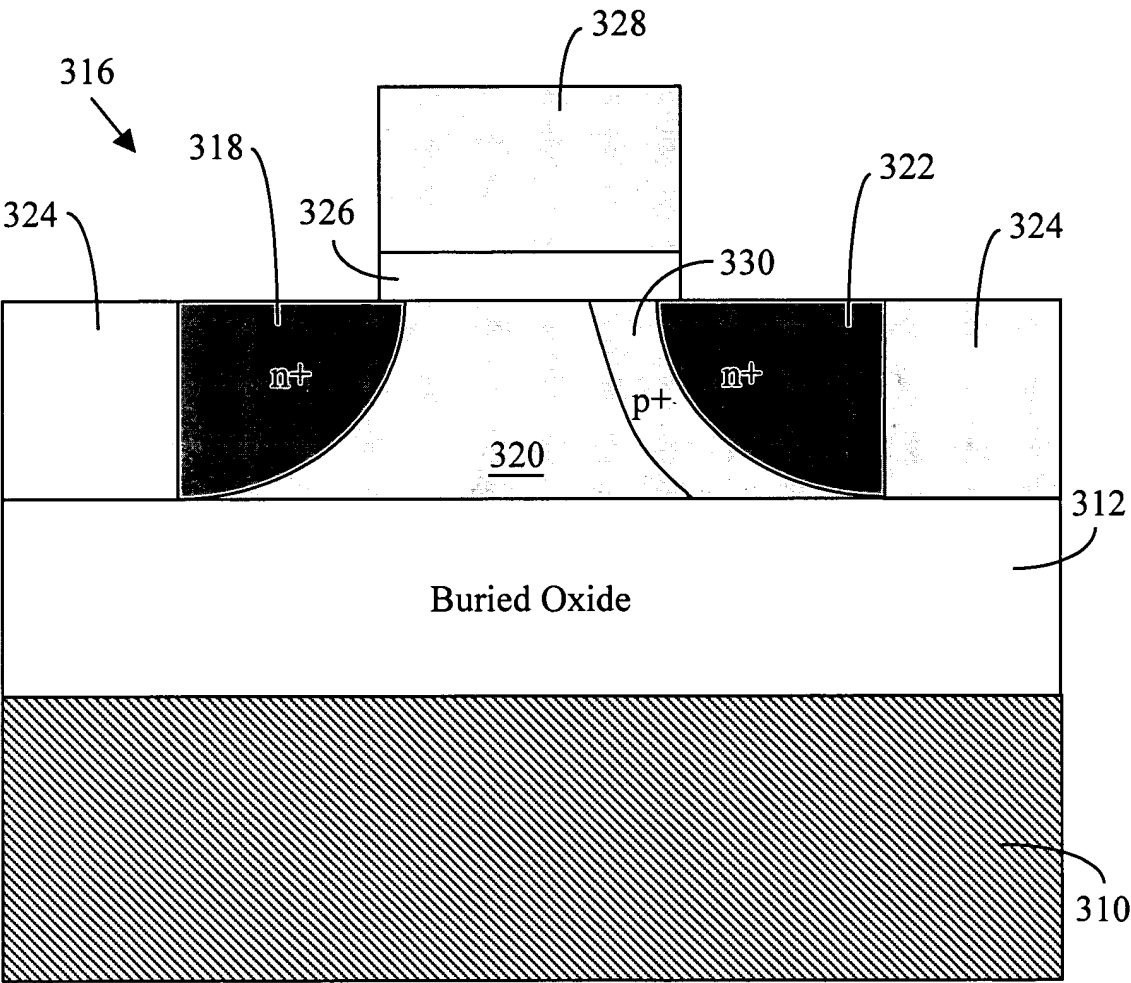


Fig. 8

Binary memory

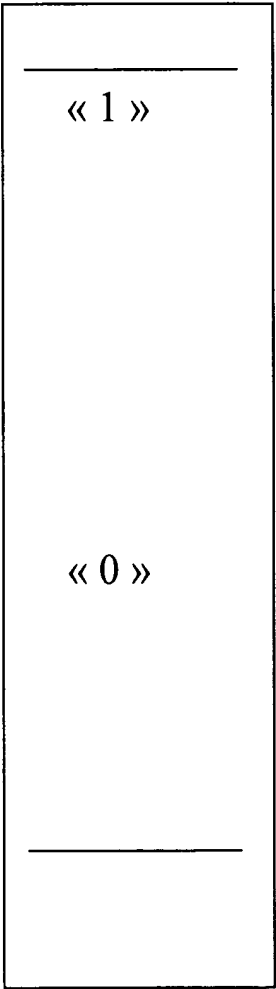


Fig. 10a

Multilevel memory

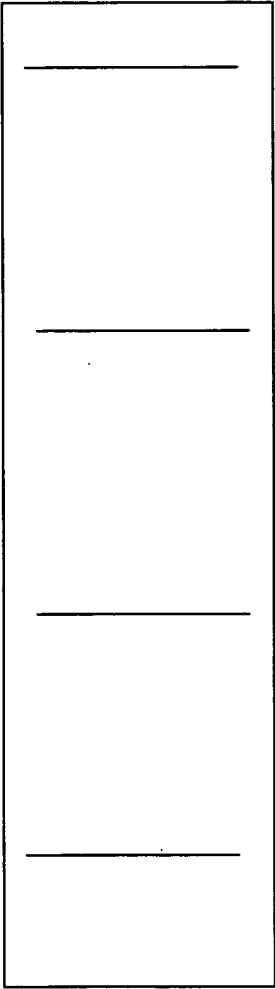


Fig. 10b

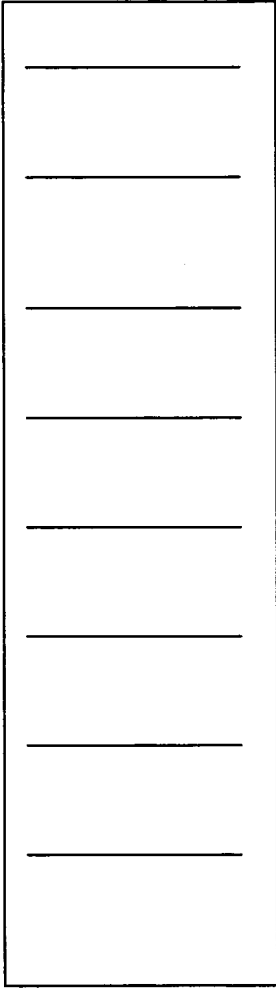


Fig. 10c

20/20

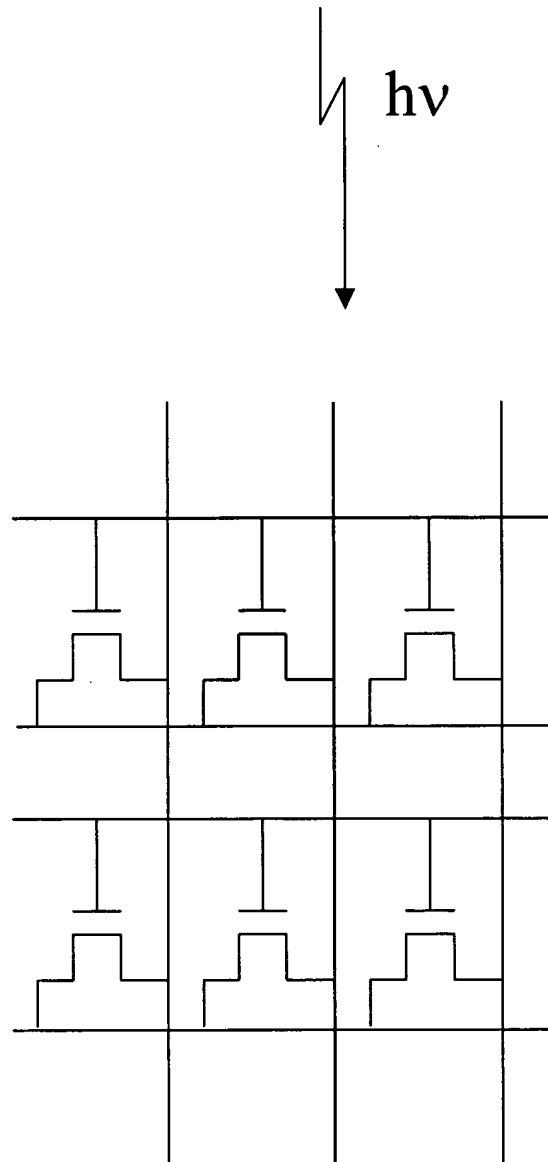


Fig. 18